V1.0

Will implement DMA with 8 bit DAC. Construct one more.

V2.0

Will implement with Surface mount components and same pic as senior design. 16 bit dAC. External oscillator. Adjust resistors for 3.3 VDD, not 5V. Implement Digipot and Graphic LCD.

Implement known issue fixes as well.

Will need breakout board for digipot.

Must built Voltage regulator Breakout as well as said PIC breakout. Build breakout for various packaging.